

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Shi et al.)	I hereby certify that this paper is
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Serial No.: 10/791,004)	States Postal Service with
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Filed: March 02, 2004)	mail in an envelope addressed to:
)	Commissioner for Patents, P.O.
For: Apparatus and Method for Performing)	Box 1450, Alexandria, VA 22313-
Generational Escape Analysis in Managed)	1450 on this date:
Runtime Environments)	
)	April 29, 2004
Group Art Unit: Unknown))	Want M Halen
Examiner: Unknown)	Mark G. Hanley
	ĺ	Registration No. 44,736
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	,	Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The publication listed on the enclosed PTO Form-1449 is submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. A copy of the publication is enclosed.

TIME OF FILING

This information disclosure statement is being filed within three months of the filing date of the application, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

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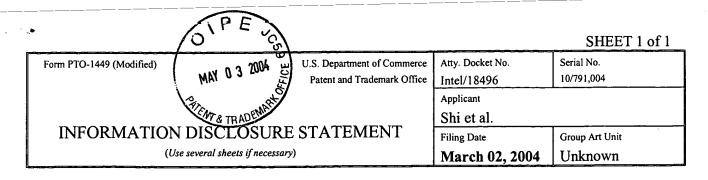
By:

Mark G. Hanley

Registration No.: 44,736

April 29, 2004

Attorney for Intel Corporation



OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
C01	Ruf, Erik, "Effective Syncronization Removal for Java", Conference on Programming Language Design and Implementation, PLDI 2000, Vancouver, Canada, pp. 208-218	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.